



CMOS Circuit Design - Analog, Digital, IC Layout

By Nicholas L Pappas Ph D

Createspace, United States, 2014. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.Electrical and Electronic Engineering Design Series Vol 3 CMOS Circcuit Design - Analog, digital, IC Layout This university level Electrical Engineering text is for anyone who wants to know how to design products using CMOS circuits. The present text is unusually accessible to readers who want to acquire the skills of CMOS circuit design as well as the skill making Integrated Circuit Chip Layouts. We present a thorough foundation so that you can proceed to learn how to design and layout CMOS circuits. This text is different from other CMOS design texts, because not only do we actually show how to design CMOS circuits selecting transistor Length, Width and the correct value of mobility (a small detail that is usually overlooked if not ignored) we show how to make accurate, functioning circuit layouts that can be used in a chip. Furthermore we ask you to work hard drawing over 60 layouts that give you real world experience. This is not about logic design. CMOS technology is the preferred technology for implementing modern digital and analog integrated circuits. We...



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